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*Jeanne Connelly*  
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SEP 13 2000  
TC 2700 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Gail A. Alverson et al.  
Application No. : 09/192,205  
Filed : November 13, 1998  
For : USER PROGRAM AND OPERATING SYSTEM INTERFACE  
IN A MULTITHREADED ENVIRONMENT

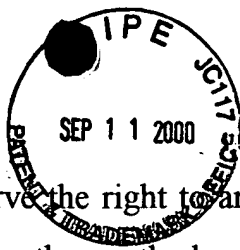
Art Unit : 2756  
Docket No. : 324758001US  
Date : September 7, 2000

Assistant Commissioner for Patents  
Washington, DC 20231

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Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO/SB/08A (copies of the cited references, as required under 37 C.F.R. § 1.98, are enclosed). Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information of which they are aware that is believed relevant to the patentability of the above-identified application, applicants believe that their invention is patentable. As to any document supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and



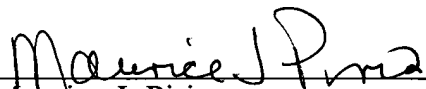
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Please acknowledge receipt of this Information Disclosure Statement and  
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Respectfully submitted,  
Perkins Coie LLP

  
Maurice J. Pirio  
Registration No. 33,273

MJP:jc  
Enclosures:  
Postcard  
Form PTO/SB/08A  
Cited References (38)

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**COMPLETE IF KNOWN**

Application Number	09/192,205
Filing Date	November 13, 1998
First Named Inventor	Gail Alverson
Group Art Unit	2756 TC 2700 MAIL ROOM
Examiner Name	
Attorney Docket No.	324758001US

Sheet

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of

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**U.S. PATENT DOCUMENTS**

*EXAMINER INITIALS	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
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	AD	5,504,932		Vassiliadis et al.	4/2/96	
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	AF					

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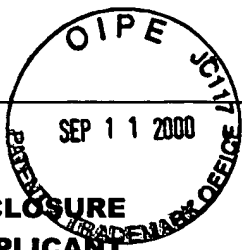
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		Office	Number	Kind Code (if known)				
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	AH	EP	EP0422945		International Business Machines	4/17/91		
	AI	EP	EP0864979		Digital Equipment Corporation	9/16/98		
	AJ	DE	DE19710252		Fujitsu Ltd.	2/26/98		
	AK	EP	EP0537098		International Business Machines	4/14/93		
	AL	EP	EP0855648		Texas Instruments, Inc.	7/29/98		
	AM	EP	EP0455966		International Business Machines	11/13/91		
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	AO	Smith, Burton, "The End of Architecture," Keynote Address Presented at the 17 <sup>th</sup> Annual Symposium on Computer Architecture, Seattle, Washington, May 29, 1990.	
	AO	Richard Korry et al., "Memory Management in the Tera MTA System," 1995.	
	AQ	Gail Alverson et al., "Processor Management in the Tera MTA System," 1995.	

**EXAMINER****DATE CONSIDERED**

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				Application Number	09/192,205
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				First Named Inventor	Gail Alverson
				Group Art Unit	2756
				Examiner Name	
Sheet	2	of	4	Attorney Docket No. 324758001US	

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	AE	Touzeau, Roy F., "A Fortran Compiler for the FPS-164 Scientific Computer," Proceedings of the ACM SIGPLAN '84 Symposium on Compiler Construction, SIGPLAN Notices 19(6):48-57, June 1984.	
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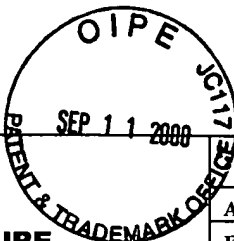
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	AF	Robert Alverson et al, "The Tera Computer System," Proceedings of 1990 ACM International Conference on Supercomputing, June 1990.	
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	AJ	Click, Cliff, "Global Code Motion, Global Value Numbering," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 30, No. 6, June 1, 1995.	
	AK	Sreedhar, Vugranam C. and Guang R. Gao, "Incremental Computation of Dominator Trees," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 30, No. 3, March 1, 1995.	
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	AE	Tera MTA Principles of Operation, November 18, 1997	
	AF	Goldman, Kenneth, J., "Introduction to Data Structures," 1996, Retrieved from Internet <a href="http://www.cs.wustl.edu/{kjc/CS101_SP97/Notes/DataStructures/structures.html">http://www.cs.wustl.edu/{kjc/CS101_SP97/Notes/DataStructures/structures.html</a>	
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	AH	H. Hayashi et al., "ALPHA: A High Performance Lisp Machine Equipped with a New Stack Structure and Garbage Collection System," 10 <sup>th</sup> Annual International Symposium on Computer Architecture, 1983.	
	AI	Smith, Burton, "Opportunities for Growth in High Performance Computing," November 1994.	
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